

PTO/SB/08A (08-00)

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Examiner Signature	<i>William M. Brewster</i>	Date Considered	<i>21 JUN 87</i>
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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	<del>Not Yet Assigned</del> <b>1A/632,930</b>		
		Filing Date	<b>August 4, 2002</b>		
		First Named Inventor	<b>Takeshi FUKUNAGA</b>		
		Group Art Unit	<b>2823</b>		
		Examiner Name	<b>W. Brewster</b>		
Sheet	<b>2</b>	of	<b>2</b>	Attorney Docket Number	<b>740756-2641</b>

<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<b>WB</b>	<b>X</b>	A.J. Auberton-Herve et al., "Industrial Research Society (Kogyo Chosa Kai); Electronic Material, pp. 83-87, August 1997 (Concise Statement attached)	<b>7</b>
<b>WB</b>	<b>X</b>	"A Dissolved Wafer Process Using a Porous Sacrificial Layer and a Lightly-Doped Bulk Silicon Etch-Stop", Bell et al., Micro Electro Mechanical Systems, 1998, MEMS 98, Proceeding., The Eleventh Annual International Workshop on 1998, pp. 251-256.	<b>2</b>

Examiner Signature	<b>William M. Brewster</b>	Date Considered	<b>21 JUN 02</b>
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